



The diagram shows two test cells, R23 and R24, connected in parallel. R23 is a 10 ohm resistor connected between F0RD and FF0RD. R24 is a 10 ohm resistor connected between F0WE and FF0WE.

Release Notation		
Date	Revision	Comments
2008.7	0.1	For SM3251QF ,First Release
2008.8	0.2	1, Change C1 and C3 from 4.7uF to 10uF. 2, delete "nReset" net.
2008.10	0.3	1, Change R26 and R27 from 0 ohm to 3.9ohm.

- Notes:
- 1, The VBUS line width should be over 24mils.
  - 2, The A3.3V line width should be over 24mils.
  - 3, The A1.8V line width should be over 24mils.
  - 4, The USB transmission line width = 8mils.
  - 5, The USB transmission line pitch between them = 12 mils.
  - 6, DP,DM pin to USB connector is as short as possible.
  - 7, PCB layout with DP / DM trace is fully symmetric.
  - 8, .FOWE,FFOWE,FORD,FFORD trace is over 10 mils

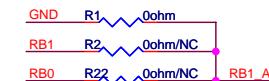
"One CE pin" & "Two CE pin" & "Four CE pin" flash setup

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| "One CE pin" Samsung K9F***** series Flash
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|      Setup as Following,
| R1   : Short
| R2   : Open
| R22  : Open
| R4   : Short
| R5   : Open
| R6   : Short
| R7   : Open
| R3   : Open

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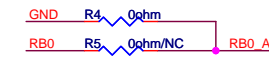


"Two CE pin" Samsung K9W\*\*\*\*\* series Flash

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Setup as Following,
R1      : Open
R2      : Short
R22     : Open
R4      : Open
R5      : Short
R6      : Open
R7      : Short
R3      : Open

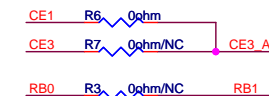
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"Four CE pin" Samsung K9N\*\*\*\* series Flash

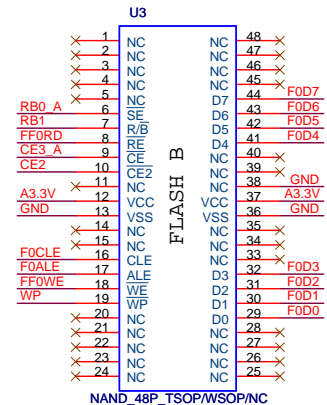
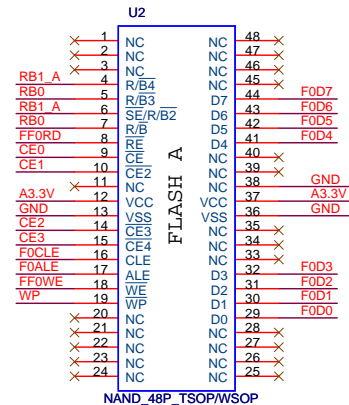
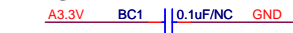
Setup as Following,

R1	: Open
R2	: Short
R22	: Open
R4	: Open
R5	: Open
R6	: Open
R7	: Open
R3	: Open



**2 FLASH**

**1 FLASH**



**Silicon Motion, INC.**

Title	SM3251QF application circuit
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Size B	Document Number SM3251QE 2FLASH DEMO V03.DSN	Rev 0.3
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